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1AP20 Rec'd PTO 11 MAY 2006

187765/US - 465122-00026

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s) : Noriyuki SUZUKI et al.
Serial No. : To be assigned
Filed : Herewith (May 11, 2006)
Entitled : PRESS-FORMING DEVICE, PRESS-FORMING METHOD,
COMPUTER PROGRAM/SOFTWARE ARRANGEMENT AND
STORAGE MEDIUM
Group Art Unit : To be Assigned
Examiner : To be Assigned

INFORMATION DISCLOSURE STATEMENT

EXPRESS MAIL NO.: EV 642 787 956 US

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), applicants bring to the attention of the Examiner the documents listed on the attached Form PTO 1449, and respectfully request that the listed documents be considered by the Examiner and made of record in the above-captioned application. Copies of the U.S. patent references listed on the Form PTO-1449 are not enclosed, however the articles are enclosed.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed documents are material or constitute "prior art." If the Examiner applies the documents as prior art against any claim in the application and applicants determine that the cited documents do not constitute "prior art" under

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United States law, applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of the documents.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should the documents be applied against the claims of the present application.

This submission is being filed together with the application. Therefore, applicants do not believe that any fee is due in connection with the submission of this paper. However, if any fee is due, or if any overpayment has been made, the Commissioner is authorized to charge any such fee or credit any overpayment, to our Deposit Account No. 50-2054.

Respectfully submitted,

Dorsey & Whitney, LLP

Date: May 11, 2006



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4833-7518-7457\1

Form PTO-1449 U.S. Department of Commerce
(REV. 2-82) Patent and Trademark Office

Atty. Docket No.
187765/US-465122-00026

Serial No. 107579544
To be assigned

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**
(Use several sheets if necessary)

Applicant(s)
Noriyuki Suzuki et al.

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Filing Date
Herewith (May 11, 2006)

Group
To be assigned

U.S. PATENT DOCUMENTS

*Exam. Init.	Document No.	Date	Name	Class	Subclass	Filing Date if Appropriate
	5 6 9 2 4 0 5	December 2, 1997	Kirii			
	5 4 1 9 1 6 9	May 30, 1995	Kirii			

FOREIGN PATENT DOCUMENT

	Document No.	Date	Country	Class	SubClass	Translator Yes No
0	5 2 8 5 7 0 0	November 2, 1993	Japan			X**
0	6 2 4 6 4 9 9	September 6, 1994	Japan			X**
0	7 2 6 5 9 5 7	October 17, 1995	Japan			X**
1	0 1 2 8 4 5 1	May 19, 1998	Japan			X**
0	8 3 0 0 0 4 8	November 19, 1996	Japan			X**
	7 2 6 6 1 0 0	October 17, 1995	Japan			X
	5 2 8 5 5 5 4	November 2, 1993	Japan			X
200	4 2 4 9 3 6 5	September 9, 2004	Japan			X**

** English language translation of abstract enclosed.

OTHER DOCUMENTS (including Author, Title Date, Pertinent Pages, Etc.)

	Yutaka Kurata "Absorption and Shield against Electromagnetic Wave" Nikkei Technical Books Corp. January 10, 1989
	Masaki Toriumi "Positioning Techniques in Semiconductor Manufacturing Equipment", 1991, pages 24-28
	Masashi Kamiya "Characteristics of a Self-acting Step-groove Gas Bearing and Its Applications" 1987, pages 46-52

4836-1072-3841\1

Examiner

Date Considered

* Examiner: Initial citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449 U.S. Department of Commerce
(REV. 2-82) Patent and Trademark Office

Atty. Docket No.
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Serial No. **10/379544**
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Applicant(s) **IAP20 Rec'd PCT/PTO 11 MAY 2006**
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Herewith (May 11, 2006)

Group
To be assigned

			G.P. Carver and W.A. Collins, "Semiconductor Measurement Technology A Manual Wafer Probe Station for an Integrated Circuit Test System" National Bureau of Standards, May 1981, pages 1-14
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4836-1072-3841\1

Examiner

Date Considered

* Examiner: Initial citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.